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Ottawa, Canada

Date: June 24, 2008

IN THE UNITED STATES PATENT AND TRADEMARK OFFICEIn re application LaChance et al.
of:

Serial No.: 10/813,060

Filed: March 31, 2004

Title: Method and Apparatus for Producing a 3-D Model of a Semiconductor
Chip from Mosaic Images

Group Art Unit: 2624

Examiner: PARK, Edward

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Mail Stop Amendment

Commissioner for Patents
United States Patent and Trademark Office
P.O. Box 1450
Alexandria, VA
22313-1450 U.S.A.**AMENDMENT UNDER 37 C.F.R § 1.116**

In response to the Final Office Action mailed April 29, 2008, the non-extended period for response being July 29, 2008, please amend the application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.